FIG.1

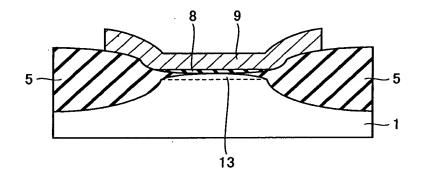


FIG.2

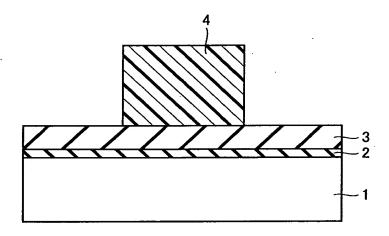


FIG.3

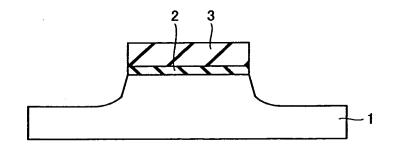


FIG.4

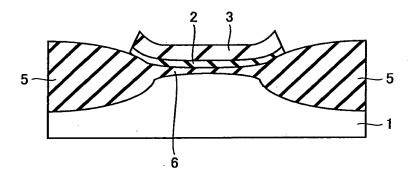


FIG.5

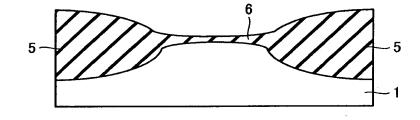


FIG.6

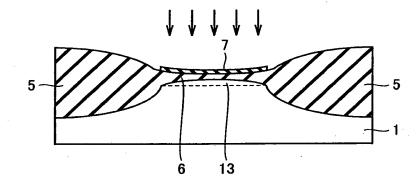


FIG.7

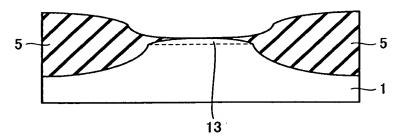


FIG.8

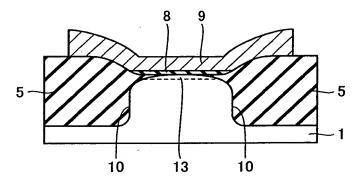


FIG.9

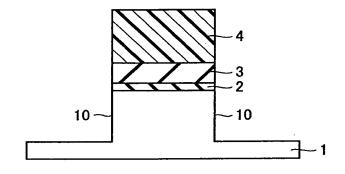


FIG.10

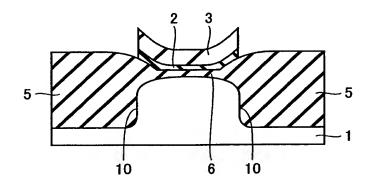
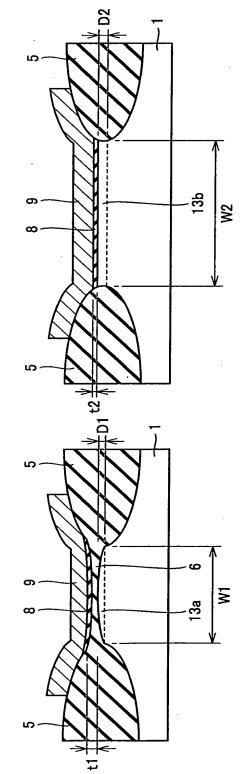


FIG.11A

FIG.11B

(ACCESS MOS TRANSISTOR PORTION)

(DRIVER MOS TRANSISTOR PORTION)



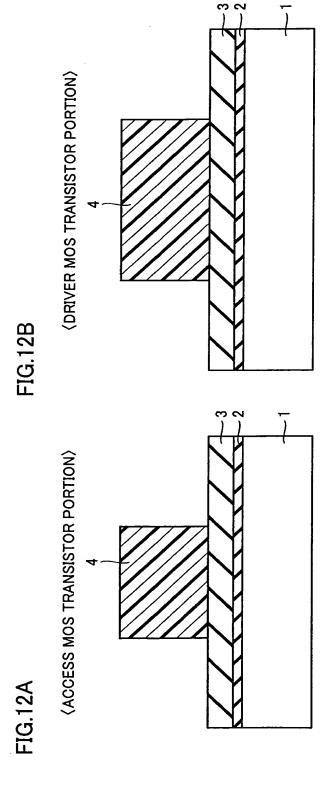
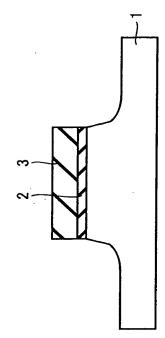


FIG.13A

FIG.13B

(DRIVER MOS TRANSISTOR PORTION)



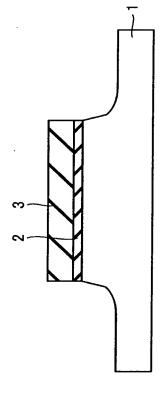
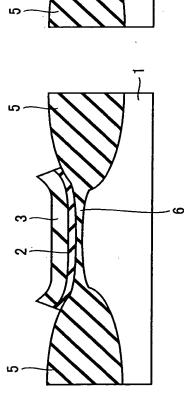


FIG.14A

FIG.14B

(DRIVER MOS TRANSISTOR PORTION)



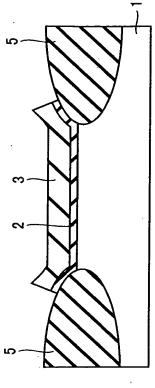


FIG.15A

FIG.15B

(DRIVER MOS TRANSISTOR PORTION)

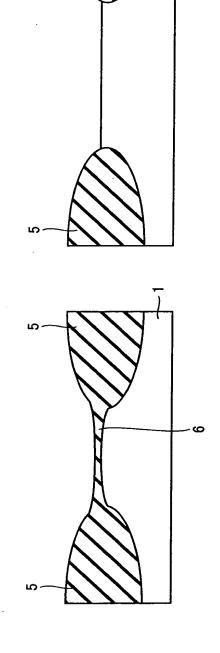


FIG.16A

FIG.16B

(DRIVER MOS TRANSISTOR PORTION)

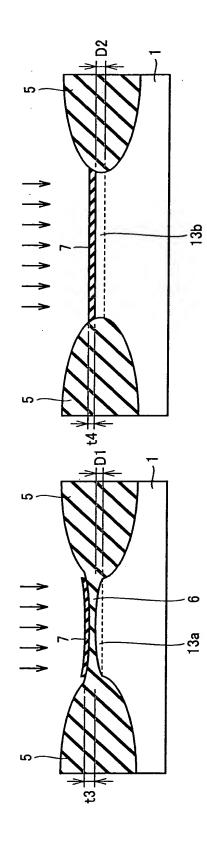
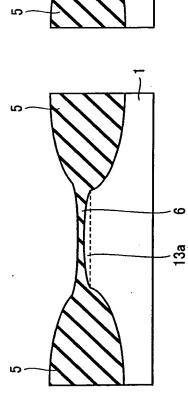


FIG.17A

FIG.17B

(DRIVER MOS TRANSISTOR PORTION)



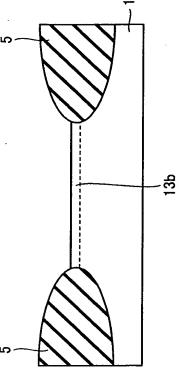
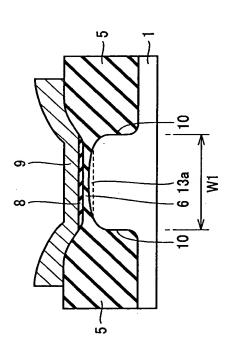


FIG.18A

FIG. 18B

(DRIVER MOS TRANSISTOR PORTION)



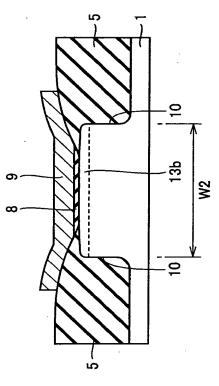


FIG.19A

FIG.19B

100

(DRIVER MOS TRANSISTOR PORTION)

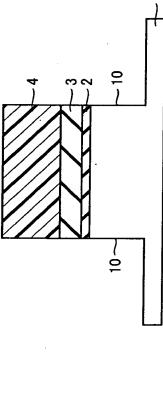
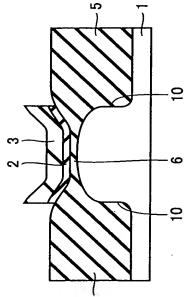


FIG.20A

FIG.20B

(DRIVER MOS TRANSISTOR PORTION)



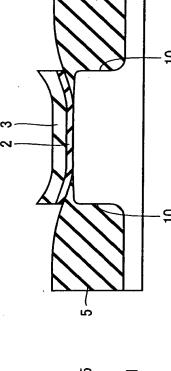


FIG.21 PRIOR ART

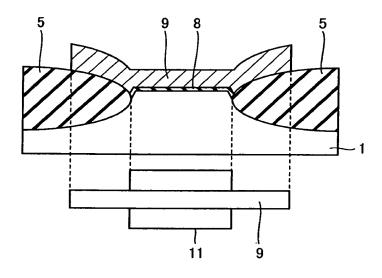


FIG.22 PRIOR ART

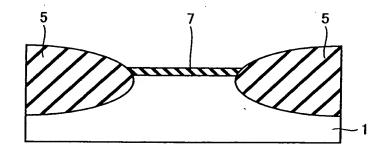


FIG.23 PRIOR ART

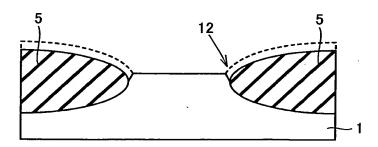
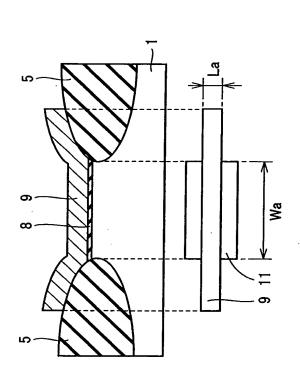


FIG.24A PRIOR ART

(ACCESS MOS TRANSISTOR PORTION)

FIG.24B PRIOR ART

(DRIVER MOS TRANSISTOR PORTION)



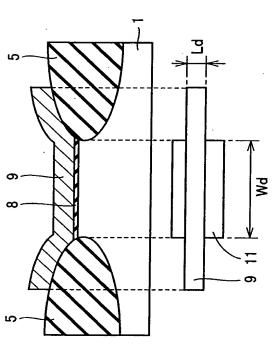
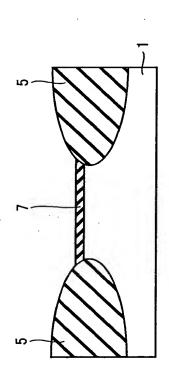


FIG.25A PRIOR ART

(ACCESS MOS TRANSISTOR PORTION)

FIG.25B PRIOR ART

(DRIVER MOS TRANSISTOR PORTION)



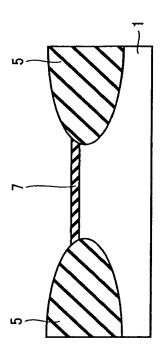


FIG.26A PRIOR ART

(ACCESS MOS TRANSISTOR PORTION)

FIG.26B PRIOR ART

(DRIVER MOS TRANSISTOR PORTION)

